FLASH MEMORY TECHNOLOGY FOR SPACE APPLICATION - AN APPROACH

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Abstract

Recently, flash memory technology has been considered for space application. This paper provides an approach for induction of the flash memory based mass data storage system to space program. It also, provides the studies and experiments that have been carried out in recent past to understand and characterize the behavior of flash memory in space environment. Further, it provides the design aspects which need to be addressed when dealing with flash memory for space environment, as identified by domain experts

Keywords: Bit Error Rate, DRAM Chips, Flash Memories, Error Correction Codes, Satellites